

8.4. Testarea SIL

8.4.1. Caracteristicile testarii SIL

The early models developed to simulate the system are tested in “model tests” (MT) and “**model-in-the-loop**” tests (MiL) – this corresponds to the simulation stage

In “**rapid prototyping**” (RP) an experimental model with high performance capacity and plenty of resources (for example, 32-bit floating point processing) is tried out in a real-life environment to check if the system can fulfill its purpose – this is also part of the simulation stage

